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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
ATTY. DOCKET NO. ROH-030

In re Patent Application of

Keiichi DEN

Serial No. Unassigned Group Art Unit: unassigned  
Filed: concurrently herewith Examiner: Unassigned  
For: SEMICONDUCTOR DEVICE OF CHIP-ON-CHIP STRUCTURE

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR 1.56

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Submitted herewith on Form PTO-1449 is a listing of documents known to Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 CFR 1.56. A copy of each listed document is being submitted to comply with the provisions of 37 CFR 1.97 and 1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a prima facie prior art reference against the claims of the present application.

Atty. Docket No. ROH-022

CONCISE EXPLANATION OF  
RELEVANCE OF EACH DOCUMENT

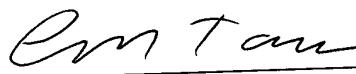
Document A1 and A2 discloses a production of semiconductor device.

The listed document is being submitted in compliance with 37 C.F.R. §1.97(B), before the mailing of the first Office action on the merits.

Applicant respectfully requests that the listed documents be considered by the Examiner and formally be made of record in the present application and that an initialed copy of Form PTO-1449 be returned in accordance with MPEP §609.

Respectfully submitted,

11-1-88  
Date

  
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